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Registered Representative

Signature

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Technology Center 2100

Patent Our Case No. 10519-60

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applica	tion of:)	
	Ilkbahar et al.)	
Serial No.:	10/024,647)) Group Art Unit:	2185
Filed:	December 14, 2001) Gloup Art Ollit.	2103
For:	Memory Device and Method for Storing Bits in Non-Adjacent Storage Locations in a Memory Array))))	

FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97-1.99, Applicants hereby submit document A1 listed on the attached form PTO-1449 for consideration by the Examiner. A copy of this document is enclosed herewith. Applicants request that the Examiner review the entire disclosure of this document and make it of record.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

Dated: December 6, 2002

Respectfully submitted,

Joseph F. Hetz

Reg. No. 41,070

Attorney for Applicants

BRINKS HOFER GILSON & LIONE P.O. Box 10395 Chicago, Illinois 60610 (312) 321-4719

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FORM PTO-1449	SERIAL NO.	CASE NO.
TANDAL TO THE PARTY OF THE PART	10/024,647	10519/60
LIST OF PATENTS AND PUBLICATIONS FOR	FILING DATE	GROUP ART UNIT
APPLICANT'S INFORMATION DISCLOSURE	December 14, 2001	2185
STATEMENT	•	2.00
(use several sheets if necessary)	APPLICANT(S): ILKBAHAR ET AL.	

REFERENCE DES	IGNATION	U.S. PATENT D	OCUMENTS		
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
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				DEC 1 8	2002
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				Technology Ce	nter 2100
					

FOREIGN PATENT DOCUMENTS

EXAM	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSI YES	LATION NO
						^

EXAMINER INITIAL		OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
	A1	"Construction Techniques for Systematic SEC-DED Codes with Single Byte Error Detection and Partial Correction Capability for Computer Memory Systems," Penzo et al., IEEE Transactions on Information Theory, Vol. 41, No. 2, March 1995, pages 584-591.

EXAMINER	DATE (CONSIDERED	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Rev. Dec.-99